

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE  
PATENT APPLICATION**

App./Confirm Nos.: 09/683,985 2597

Atty. Docket: BUR920020014US1

Applicant: Steven Voldman

Today's Date: August 9, 2007

Filing Date: March 8, 2002

Examiner: Ori Nadav

Title: **A METHOD OF FORMING LOW  
CAPACITANCE ESD ROBUST DIODES**

Group Art Unit: 2811

**Amendments Under 37 C.F.R 1.114**

**Amendment G**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated April 11, 2007, Applicant respectfully requests reconsideration of the outstanding rejections and reexamination of the present application in light of the following remarks and amendments.